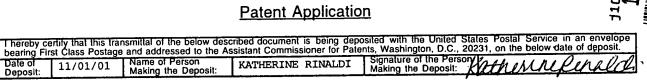
Attorney Docket No.: CYPR-CD00184





In re Application of:

Inventor(s): Craig Nemecek
Application No.: 09/975,105

Filed: 10/10/01

Title: HOST TO FPGA INTERFACE IN AN IN-CIRCUIT EMULATION SYSTEM

Assistant Commissioner for Patents

Washington, D.C. 20231

CONDITIONAL PETITION FOR EXTENSION OF TIME

This conditional petition is being filed concurrently with the filing of a <u>CONTINUATION-IN-PART PATENT APPLICATION</u>

and provides for the possibility that applicant has inadvertently overlooked the need for a petition and fee for extension of time.

Conditional petition for extension of time

If any extension of time for the accompanying response is required, applicant requests that this be considered a petition therefor.

Status

| This ap | plication is on behalf of |
|---------|--|
| | other than a small entity a small entity A verified statement: is attached is already filed |
| [x] | The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085. A duplicate copy of this authorization is enclosed. |

rev. 12/97 dbp

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060

Respectfully submitted,

Date: // // 2001

By:_

Anthony C. Murabito Reg. No. 35,295